

RESPONSE UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE GROUP ART UNIT 2672

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application. No: 10/086,174			
Filed:	February 28, 2002		
Inventor(s):			
Michael G. Lavelle, Ewa			
Kubalska & Yan Yan Tang			
Title:	Active Block Write-Back		
	from SRAM Cache to		
	DRAM		

Examiner:	Chauhan, Ulka J.
Group/Art Unit:	2676
Atty. Dkt. No:	5181-86900
•	P6060

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Mark K. Brightwell

Mark K. Brightwell

Aug. 25, 2005

Signature

Date

AMENDMENT AFTER FINAL REJECTION RESPONSE TO OFFICE ACTION OF May 25, 2005

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of May 25, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.